

004201-02290960

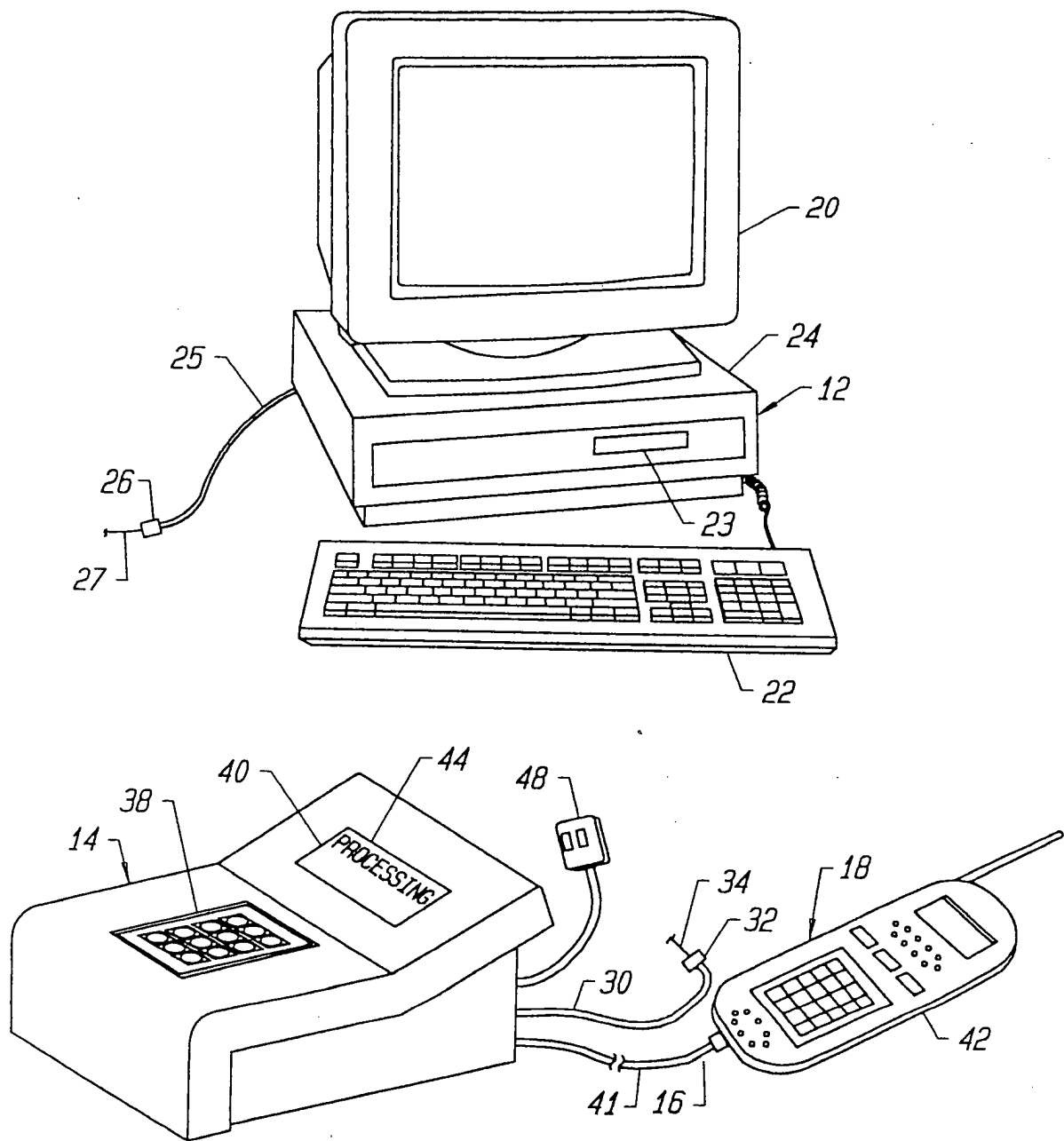


FIG. 1

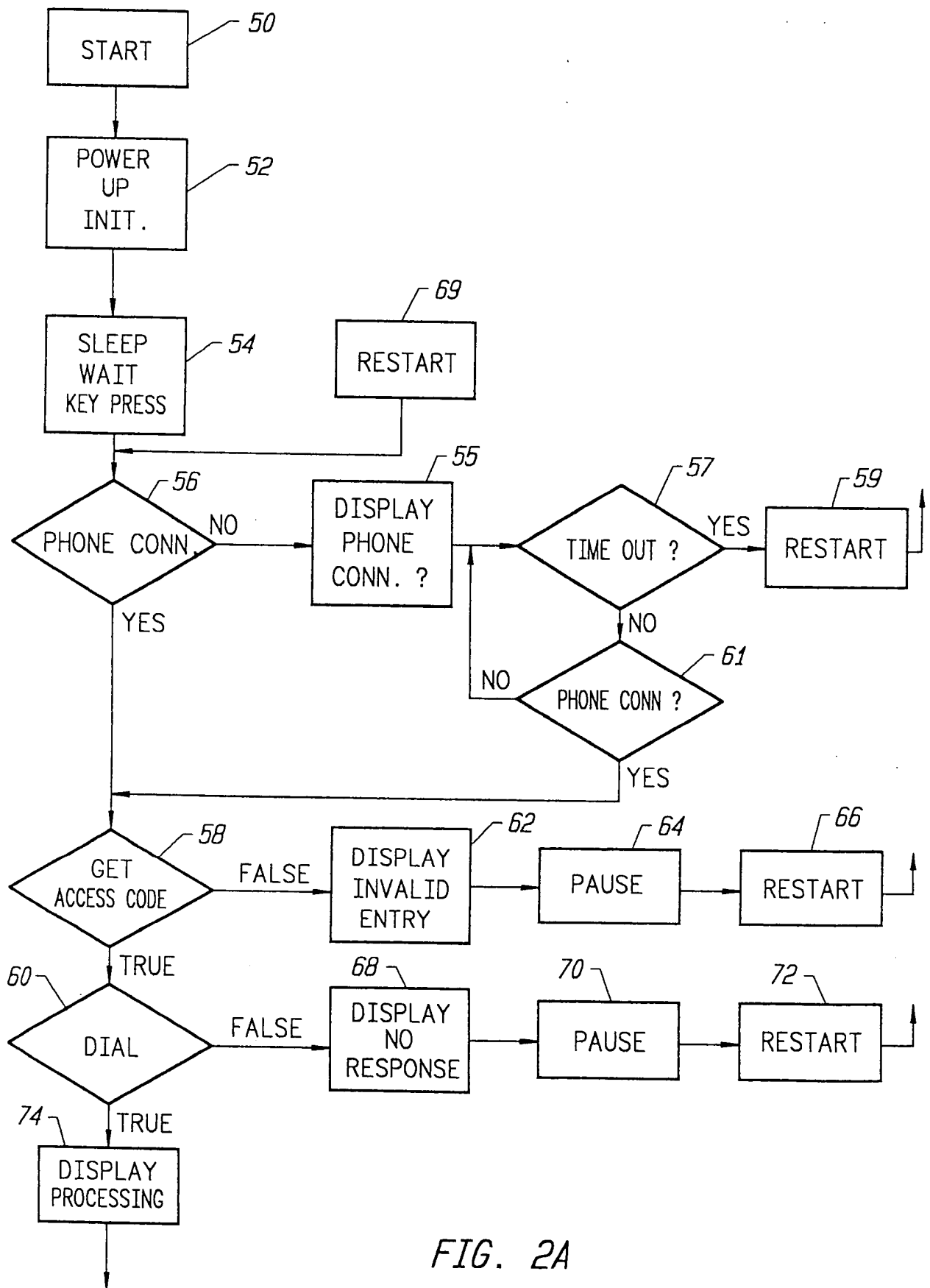


FIG. 2A

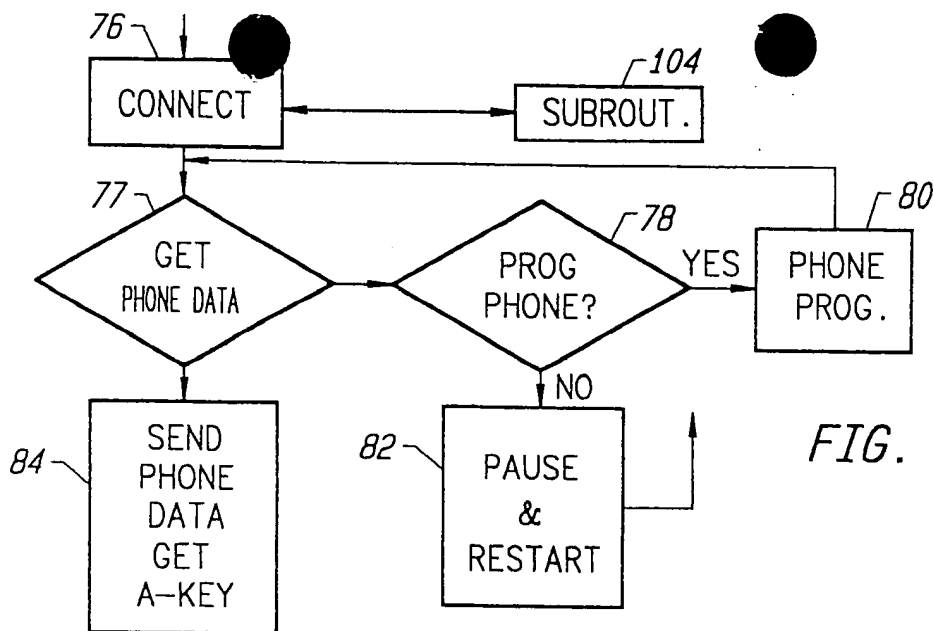


FIG. 2B

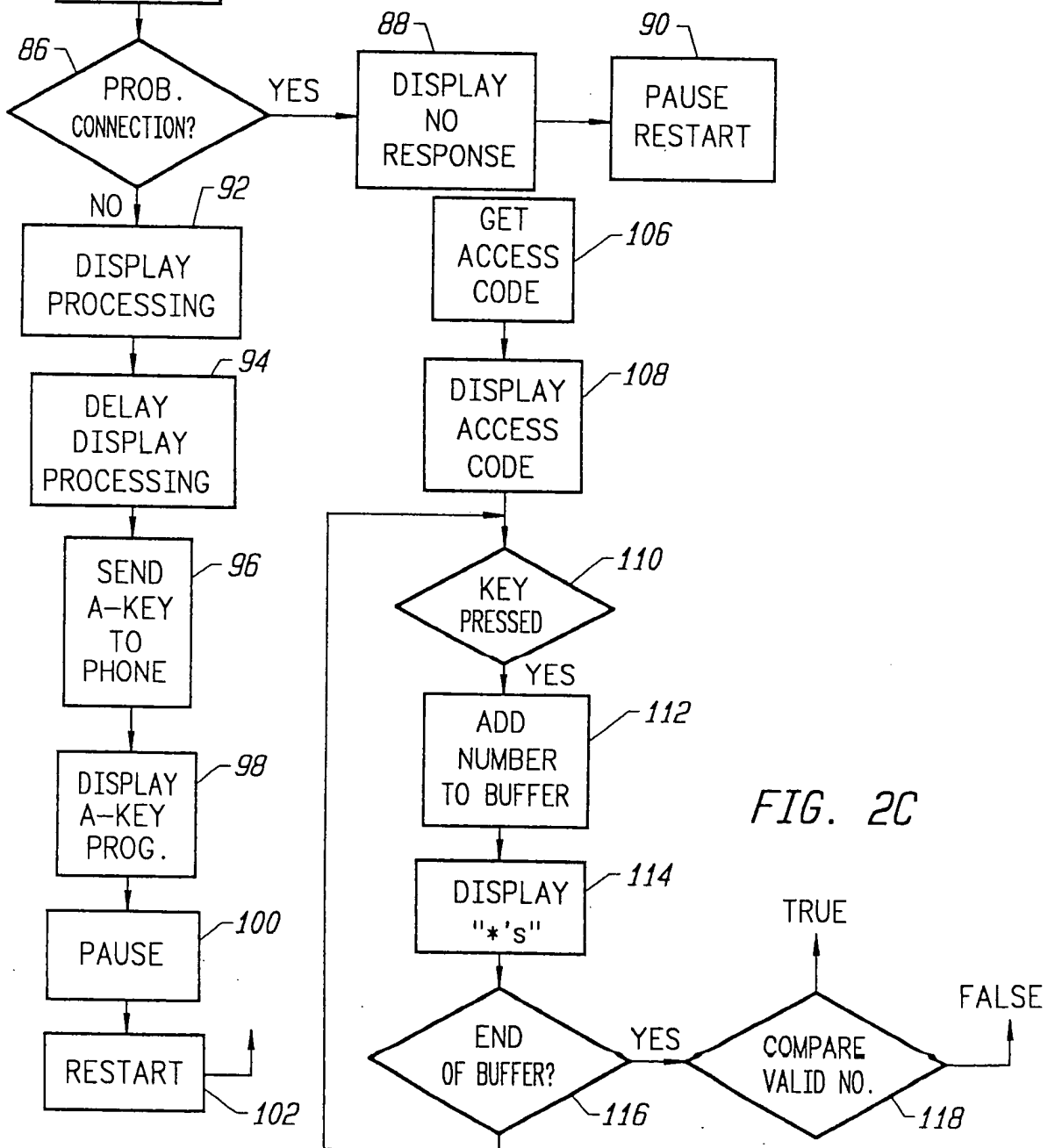
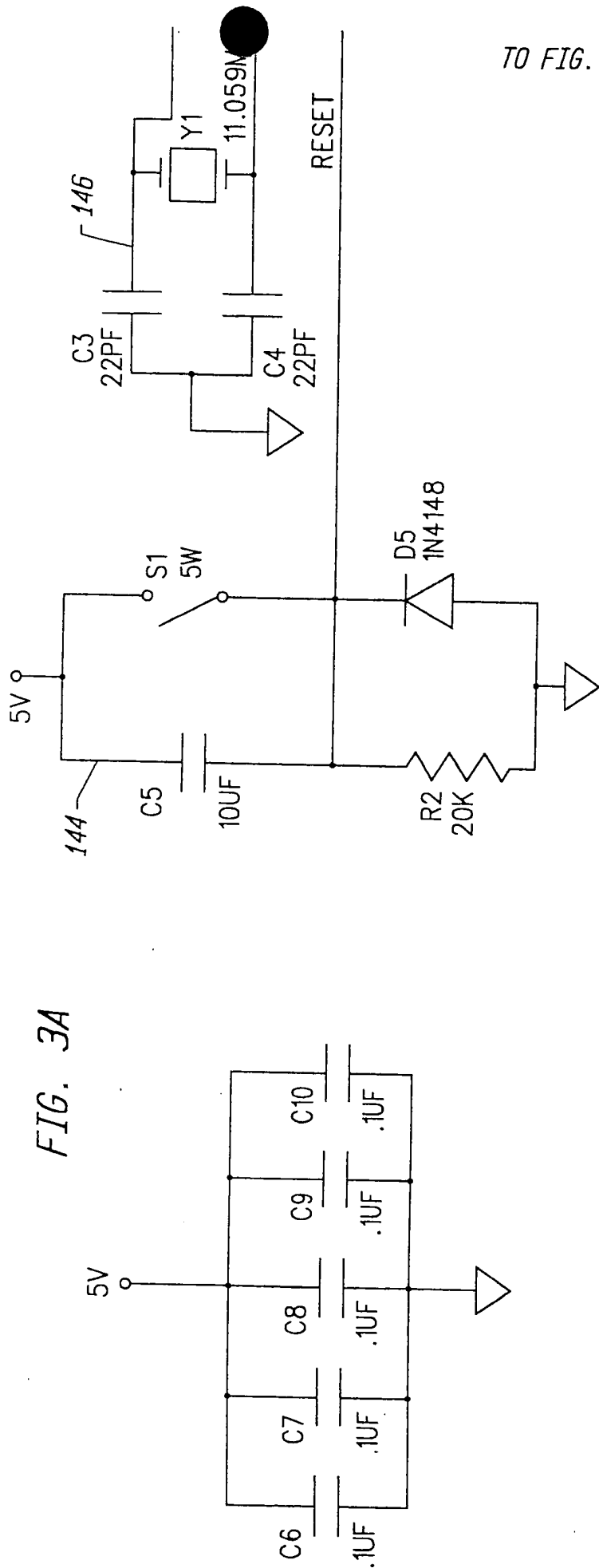


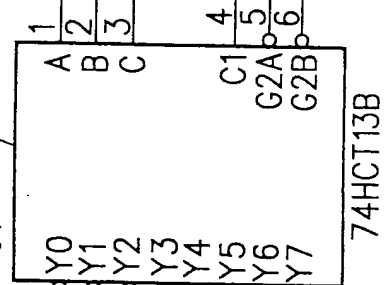
FIG. 2C

FIG. 3A



TO FIG. 3E

U1 140



TO FIG. 3B

TO FIG. 3F

TO FIG. 3A

FIG. 3B

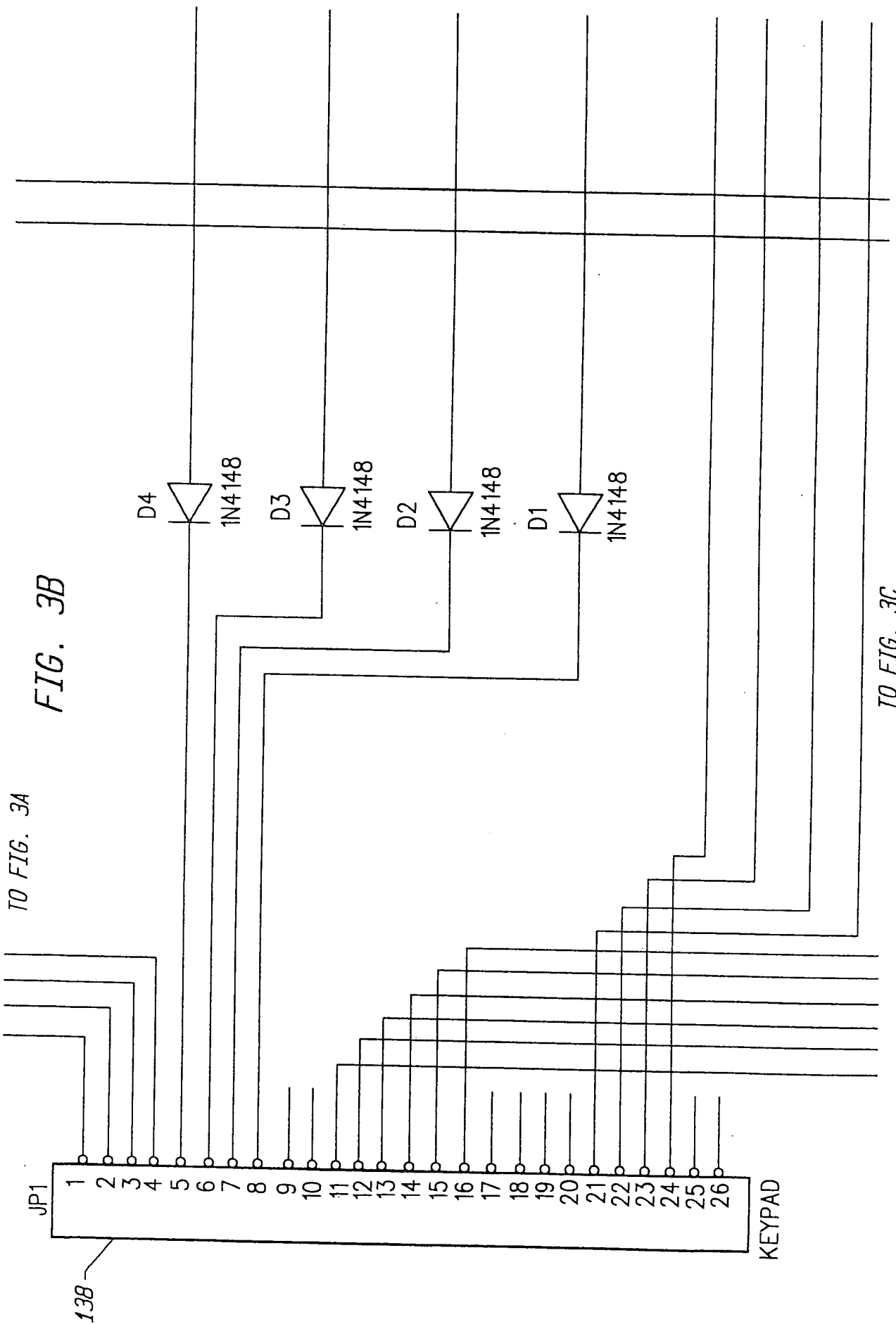


FIG. 3F

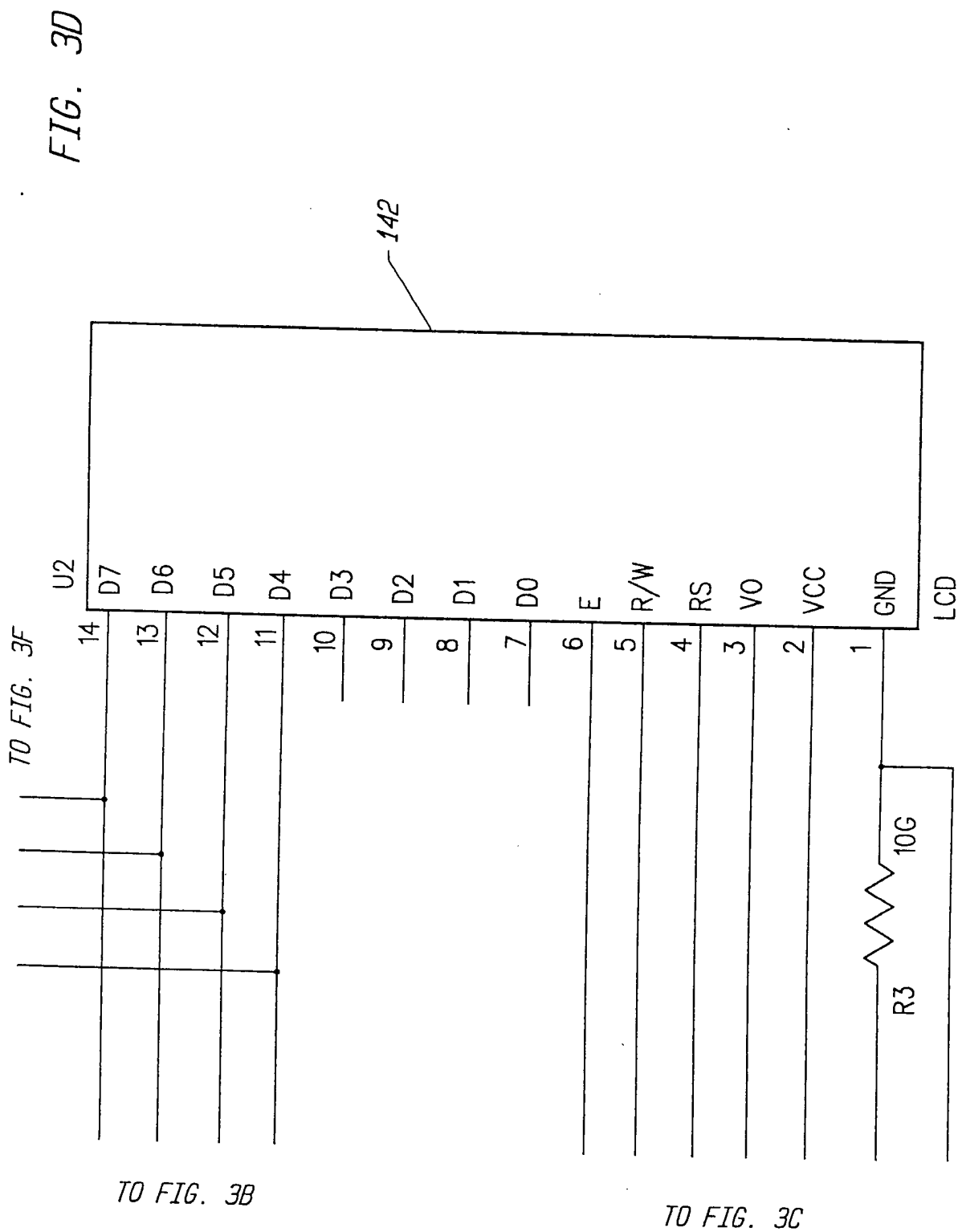
TO FIG. 3D

TO FIG. 3C

TO FIG. 3D

TO FIG. 3B

The diagram illustrates a circuit with a 5V DC source. The source is connected to a network of resistors and a potentiometer. A 20K resistor is connected in series with the source. This is followed by a potentiometer (POT) and another 20K resistor (R1). The output of the circuit is connected to a triangular symbol, which likely represents a load or a measurement point. The circuit is drawn with multiple horizontal lines representing the power rails.



DATA BUS

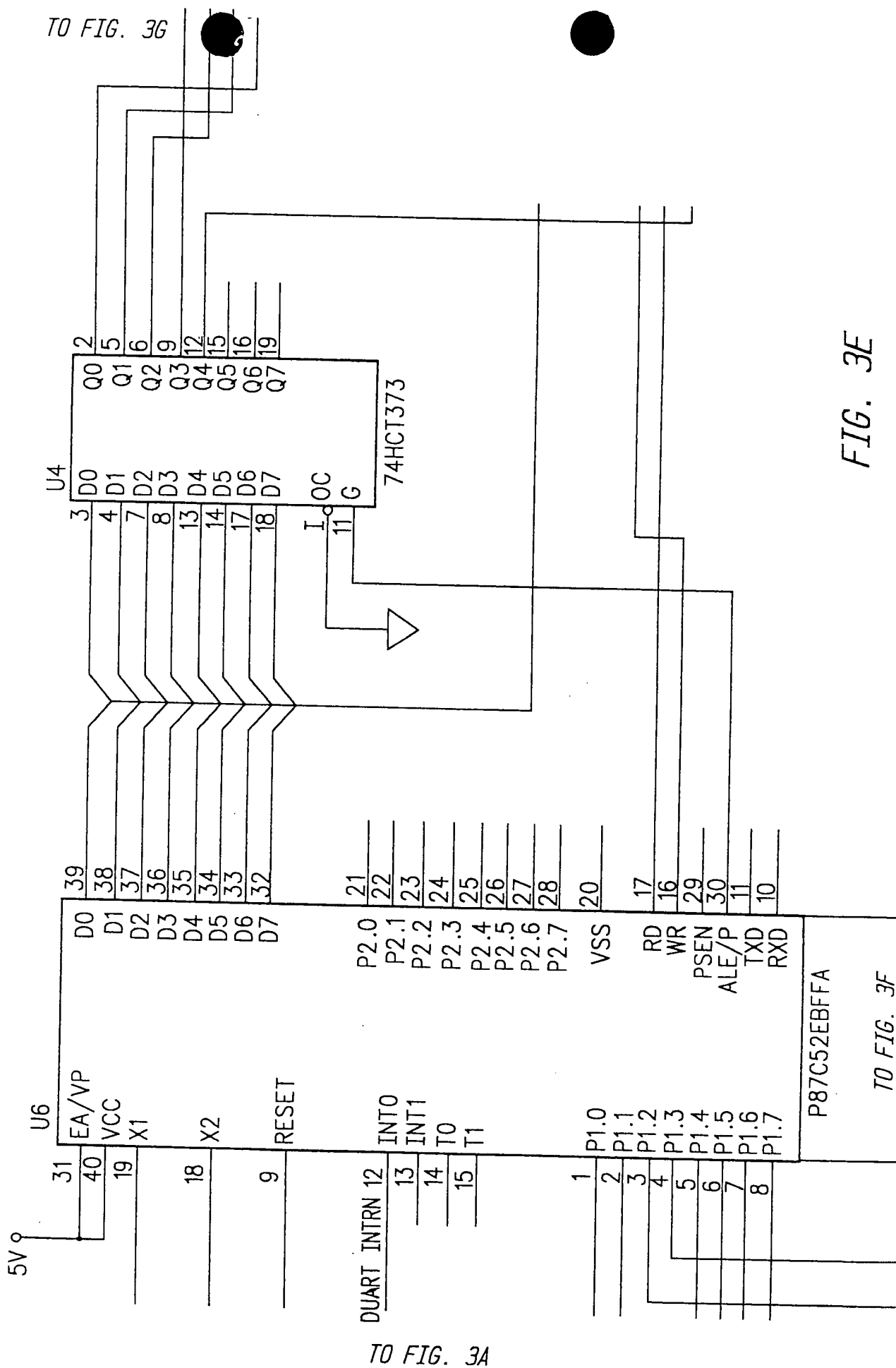


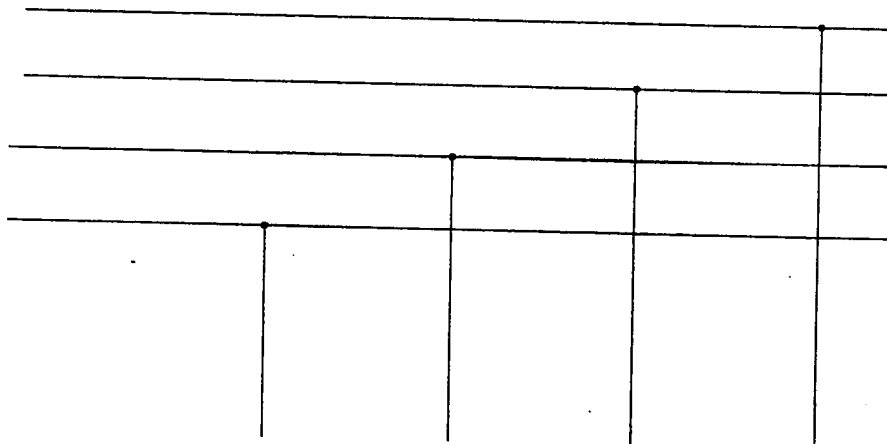
FIG. 3E



[illegible]

FIG. 3F

TO FIG. 3A



TO FIG. 3E

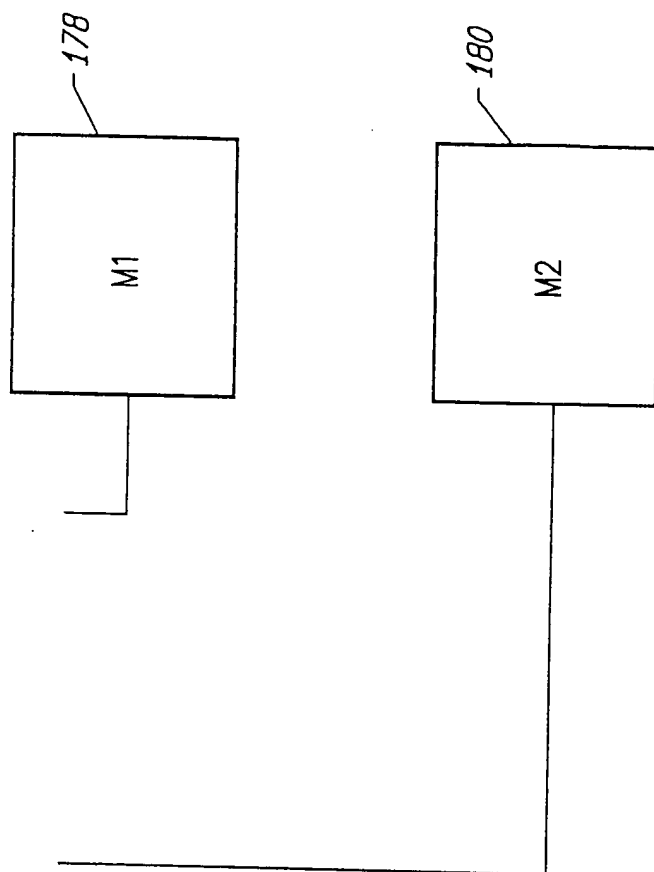
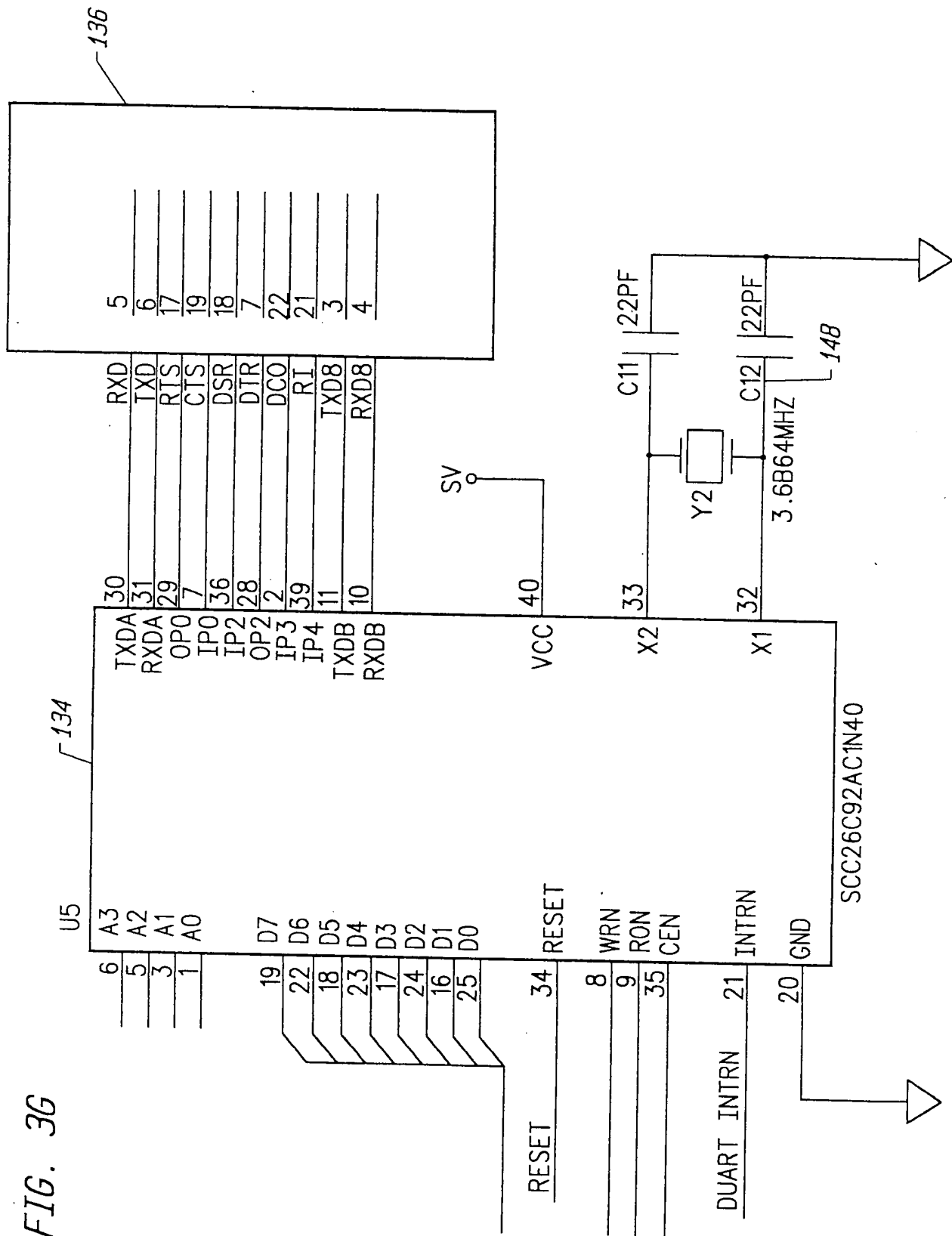


FIG. 3G



TO FIG. 3E

FIG. 4

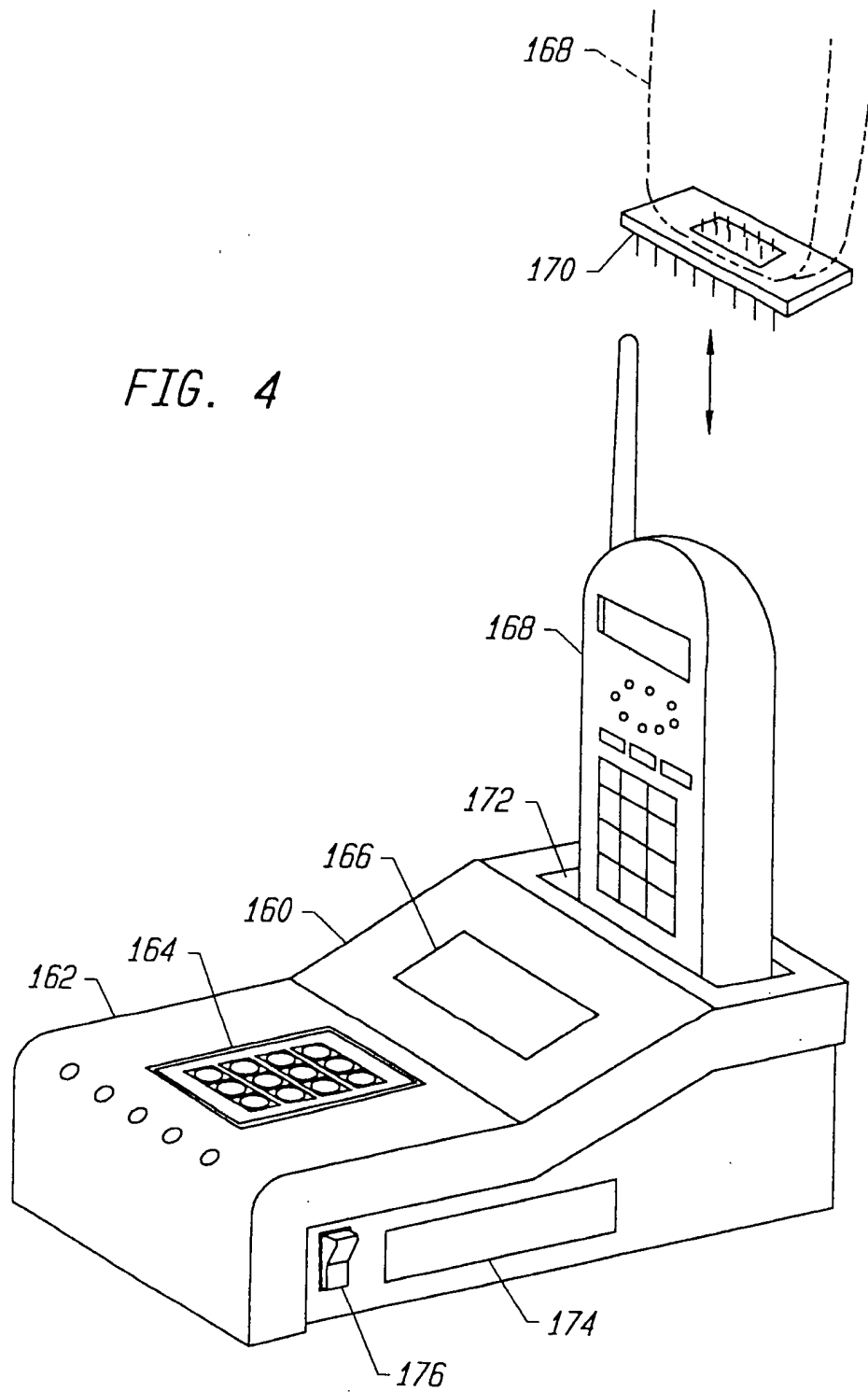


FIG. 5A

